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Sunles

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q67964

Masamoto TAGO, et al.

Appln. No.: 10/043,225

Group Art Unit: 2811

Confirmation No.: 4956

Examiner: Nitin PAREKH

Filed: January 14, 2002

For: SEMICONDUCTOR DEVICE, MANUFACTURING METHOD AND APPARATUS
FOR THE SAME

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APR 22 2003
TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

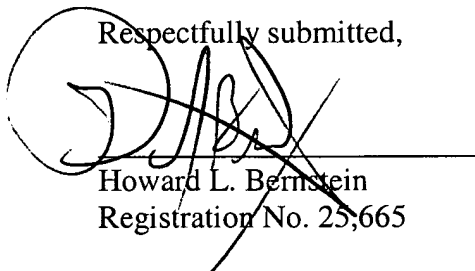
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated March 19, 2003 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-56 and 158, drawn to a semiconductor device. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,


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WASHINGTON OFFICE



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PATENT TRADEMARK OFFICE

Date: April 21, 2003